

Fig. 1 Reflection of the BCD cell vs the voltage of switching pulse.

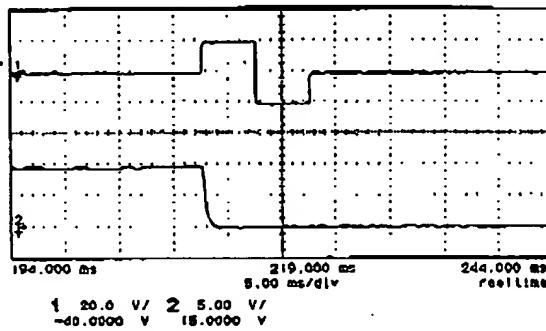


Fig. 2 The switching of the BCD from the P state to FC state. The response time is about 1 ms.

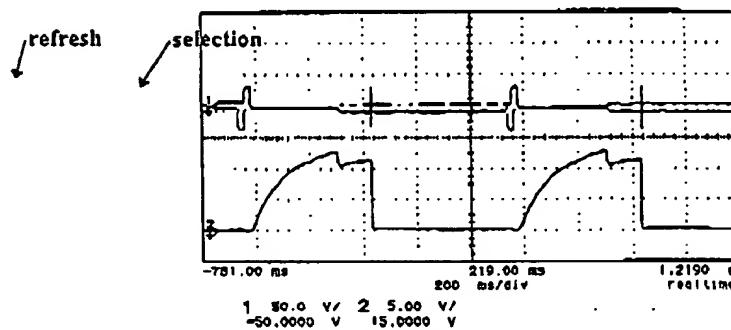


Fig. 3 Switching history between the P and FC states.

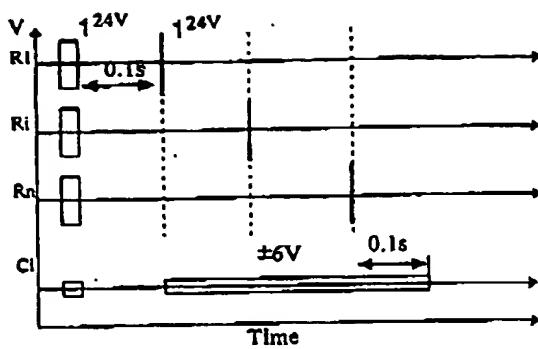


Fig. 4 Timing sequence of the driving scheme.  $R_i$  is the row signal and  $C_i$  is the column signal.

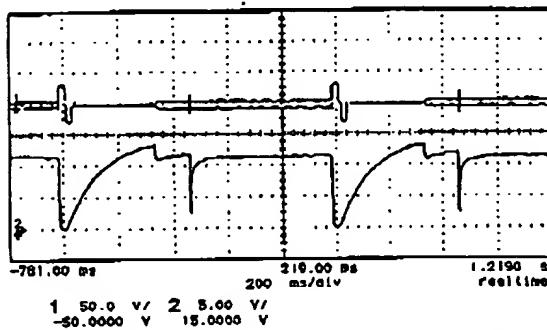
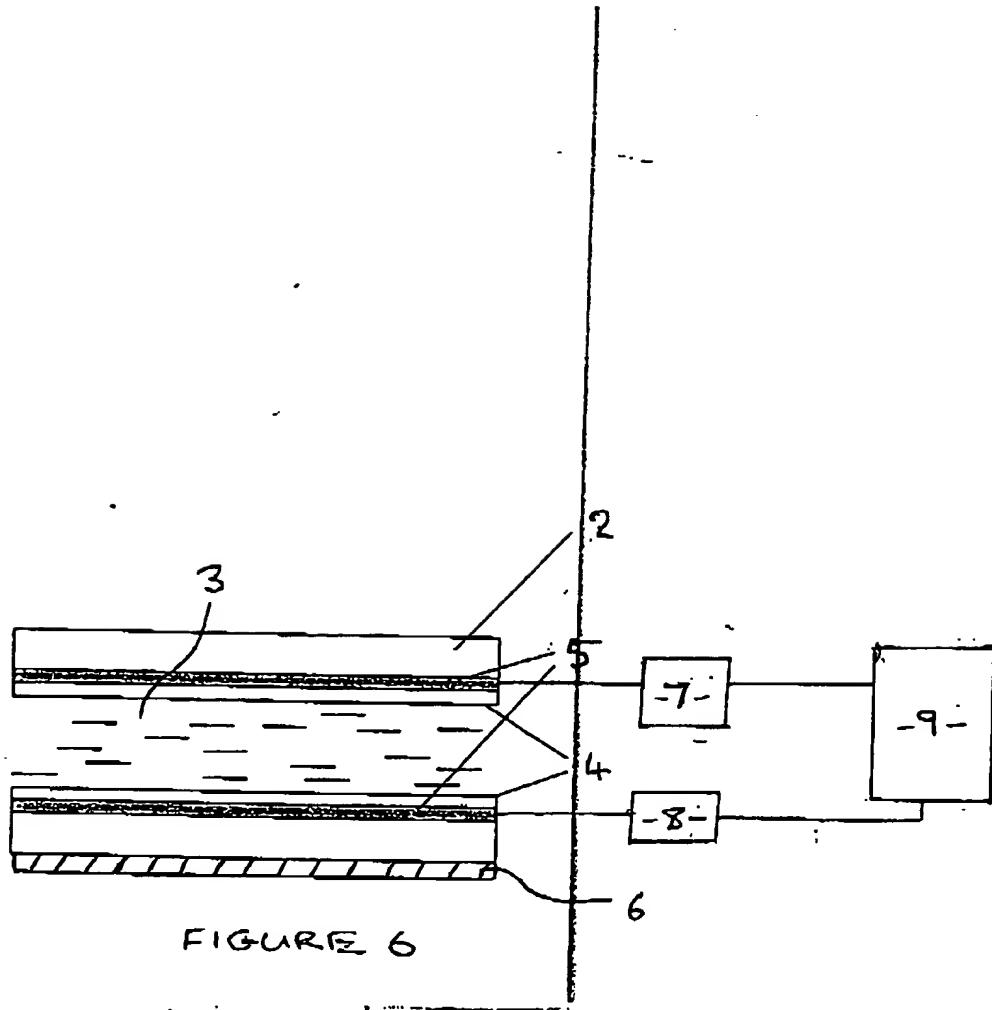


Fig. 5 Influence of the nonselection pulse train on the P state pixel.



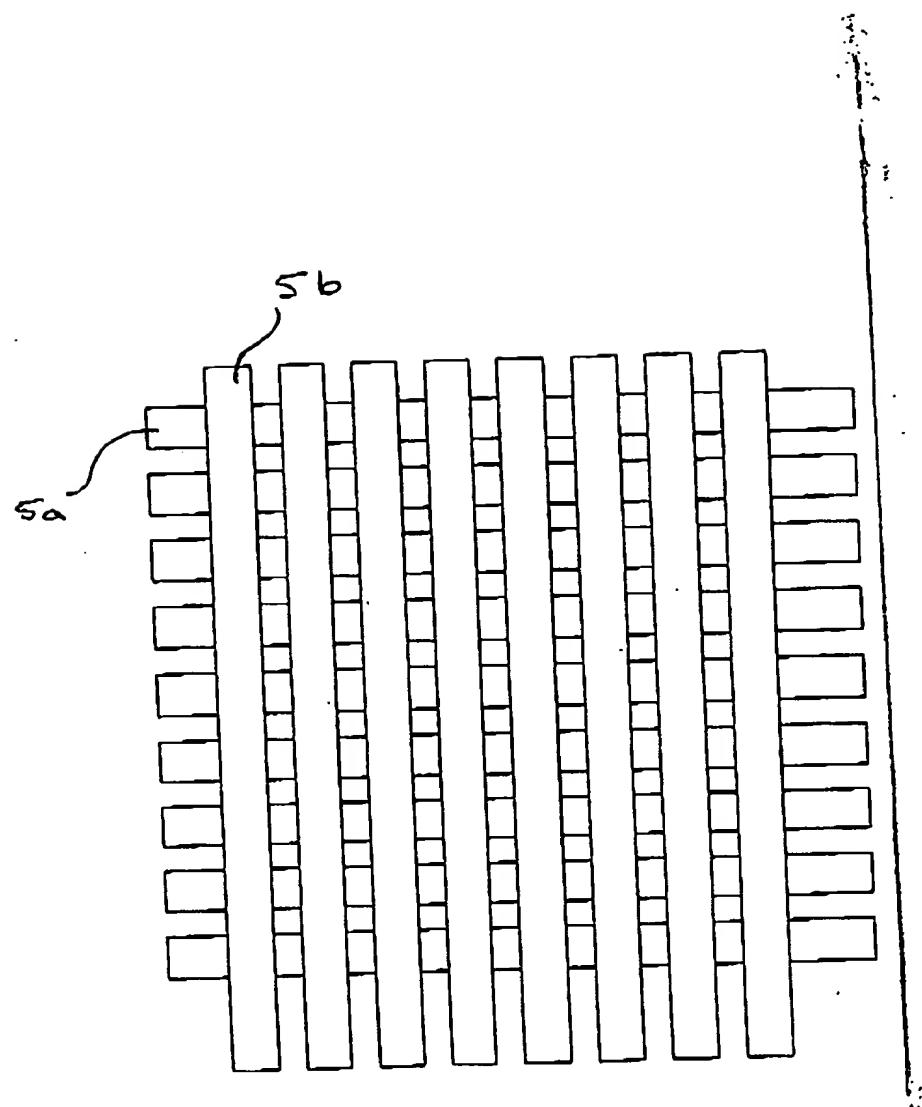


FIGURE 7